Semiconductor UK

	PERMANENT per annum - Contract rates available on request									
Experience	Graduate MSc	Graduate PhD	Mid - level	Senior	Principal	Senior Principal	Technical Team Lead	Technical Manager (team of 5+	Eng. Director/ Design Centre Manager/	Q
	1150	FIID	2 - 5yrs	6 - 10yrs	11 - 15yrs	12 - 20yrs	15+yrs	engs)	VP Eng	
Digital IC Design										
	£40 - 45,000	£45 - 50,000	£50 - 60,000	£60 - 75,000	£75 - 95,000	£85 - 120,000	£90 - 120,000	£100 - 120,000	£130,000+	,
Digital IC Verification	£40 - 45,000	£45 - 50,000	£50 - 60,000	£60 - 75,000	£75 - 95,000	£85 - 120,000	£90 - 120,000	£100 - 120,000	£130,000+	/
Physical Design	£40 - 45,000	£45 - 50,000	£50 - 60,000	£60 - 75,000	£75 - 95,000	£85 - 120,000	£85 - 120,000	£100 - 120,000	-	
FPGA Design	£38 - 45,000	£45 - 50,000	£50 - 55,000	£55 - 70,000	£70 - 80,000	£80 - 90,000	£85 - 95,000	£95 - 130,000	-	
Analog/Mixed Signal IC Design	£40 - 45,000	£45 - 50,000	£50 - 65,000	£65 - 75,000	£75 - 90,000	£85 - 110,000	£95 - 120,000	£110 - 130,000	£130,000+	L
RF IC Design	£40 - 45,000	£45 - 50,000	£50 - 65,000	£65 - 75,000	£75 - 90,000	£85 - 110,000	£95 - 120,000	£110 - 130,000	£130,000+	
Analog / RF Layout	£35 - 40,000	£40 - 45,000	£45 - 60,000	£60 - 70,000	£70 - 80,000	£75 - 90,000	£90,000+	£90 - 100,000	-	
IC Test	£30 - 35,000	£30 - 40,000	£40 - 50,000	£50 - 65,000	£65 - 75,000	£75 - 85,000	£80 - 90,000	£90 - 100,000	£100,000+	(
IC Process	£28 - 33,000	£28 - 35,000	£35 - 45,000	£45 - 60,000	£60 - 70,000	£70,000+	-	£75,000+	£85,000+	
Post Silicon Validation	£27 - 33,000+	£34 - 45,000+	£45 - 55,000+	£55 - 70,000+	£70 - 80,000+	£75,000+	£80,000+	£80,000+	£100,000+	
Integrated Photonics	£25 - 35,000	£35 - 45,000	£45 - 55,000	£55 - 75,000	-	£70 - 85,000	£70 - 90,000	£80.000 +	£100,000+	_